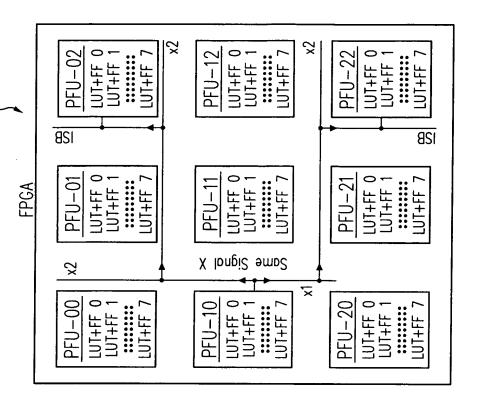
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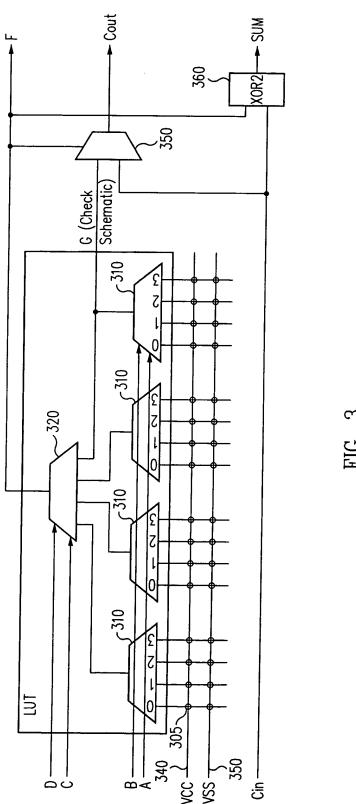


LUT+FF 0 LUT+FF 1 LUT+FF 7 LUT+FF 0 LUT+FF 1 LUT+FF C LUT+FF 1 **LUT+FF** 881 ISB 720 LUT+FF 0 LUT+FF 1 LUT+FF 0 LUT+FF 1 -UT+FF 7 **FPGA** LUT+FF (LUT+FF) X longi2 A χ Ξ LUT+FF 0 LUT+FF 1 LUT+FF 0 LUT+FF 1 LUT+FF 7 LUT+FF C LUT+FF 1

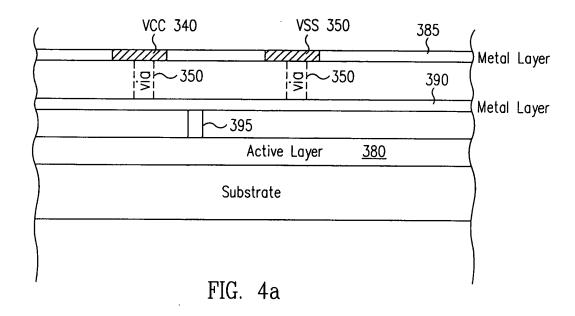
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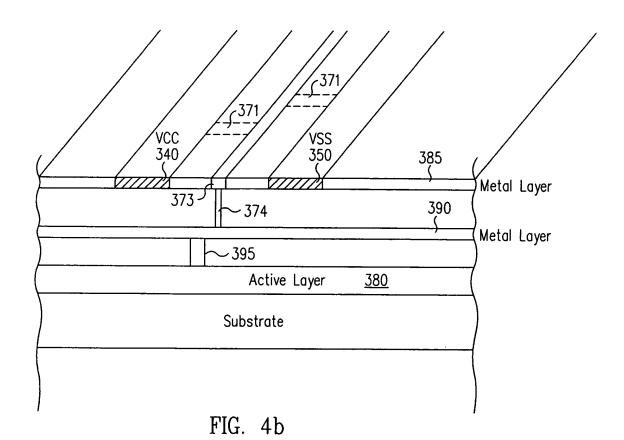
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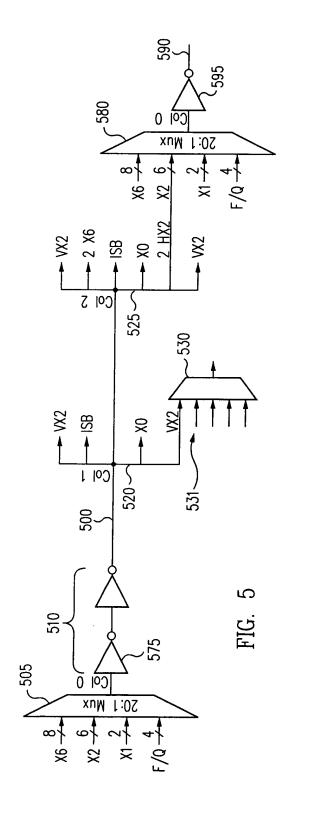
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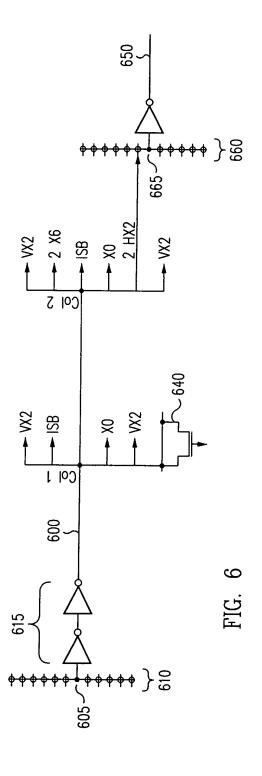




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4/5





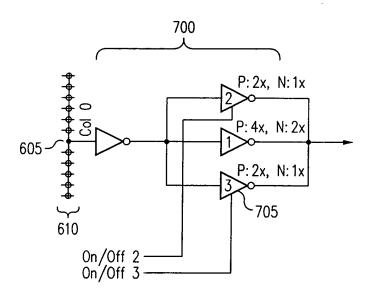


FIG. 7